

Layouts For The Shuffle-exchange Graph And Lower Bound Techniques For VLSI (MIT/LCS/TR) By Frank Thomson Leighton

By Frank Thomson Leighton

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3. The Shuffle-Exchange Graph The shuffle-exchange graph on n vertices is defined only when n is a power of two. Each of the $n/2^k$ vertices can be identified with an

<http://people.csail.mit.edu/cel/resume/papers/Layout-for-ShuffleExch.pdf>

CiteSeerX - Scientific documents that cite the following paper: New layouts for shuffle-exchange graph, in

<http://citeseerx.ist.psu.edu/showciting?cid=3385140>

Complexity Issues in VLSI. In particular, it describes optimal layouts for the shuffle-exchange graph, one of the best known networks for parallel computation.

<https://mitpress.mit.edu/index.php?q=books/complexity-issues-vlsi>

SIAM Journal on Algebraic Discrete Methods. Article Tools. Add to my favorites. Layouts for the Shuffle-Exchange Graph Based on the Complex Plane Diagram.

<http://epubs.siam.org/doi/abs/10.1137/0605021>

Critical Problems in Very Large Scale Computer Systems. Uploaded by Charles Leiserson. 1 of 2: Info; Abstract: Abstract: This is the first

http://www.academia.edu/2894028/Critical_Problems_in_Very_Large_Scale_Computer_Systems

Abstract: This paper describes a technique for producing a VLSI layout of the shuffle-exchange graph. It is based on the layout procedure which lays out a graph by
http://www.academia.edu/2435048/A_layout_for_the_shuffle-exchange_network

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Bisection bandwidth. From Wikipedia, the on the bisection width of a computationally-important variant of the De Bruijn graph known as the shuffle-exchange graph.
http://en.wikipedia.org/wiki/Bisection_bandwidth

new lower bound techniques for vlsi: mit-lcs-tm-227: layouts for the shuffle-exchange graph based on the complex plane diagram: mit-lcs-tm-221: leighton, frank
http://publications.csail.mit.edu/lcs/viewpubs.php?permit_indexing&cat=tm&sort=docnum&count=2000

UPPER BOUND FOR QUEUE NUMBER In this paper we prove that Shuffle Exchange graph has a 3-queue layout, Suppose we have a linear graph layout
<http://actamath.savbb.sk/pdf/acta1106.pdf>

MIT LCS Technical Memo TM-116, Frank Thomson Leighton, we present several new layouts for the shuffle-exchange graph,
http://dl.acm.org/ft_gateway.cfm?id=802492&type=pdf&coll=portal&dl=ACM

Frank Thomson Leighton, optimal layouts for the shuffle-exchange graph and other networks, MIT Press "Area-efficient graph layouts (for VLSI)," in
<http://dl.acm.org/citation.cfm?id=1109557.1109670&preflayout=flat>

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bin/GetTRDoc?Location=U2&doc=GetTRDoc.pdf&AD=ADA121538](http://www.dtic.mil/cgi-bin/GetTRDoc?Location=U2&doc=GetTRDoc.pdf&AD=ADA121538)

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p\$. , INSTITUTION="MIT Laboratory for Computer Science"
<http://edas.info/netbib/net92.bib>

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<http://www.jstor.org/stable/pdfplus/2030515.pdf>

Shuffle-exchange and cube-connected cycles F.T. Leighton, M. Lepley and G.L.
Miller, An asymptotically optimal layout for the shuffle-exchange graph, J
<http://www.sciencedirect.com/science/article/pii/0020019086901225>

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